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Data Sheet: T1H–CTRIO–DS Rev A

## Terminator I/O

T1H–CTRIO Counter I/O Module (use base T1K–16B or T1K–16B–1)



**WARNING:** To minimize the risk of potential safety problems, you should follow all applicable local and national codes that regulate the installation and operation of your equipment. These codes vary from area to area and it is your responsibility to determine which codes should be followed, and to verify that the equipment, installation, and operation are in compliance with the latest revision of these codes.

Equipment damage or serious injury to personnel can result from the failure to follow all applicable codes and standards. We do not guarantee the products described in this publication are suitable for your particular application, nor do we assume any responsibility for your product design, installation, or operation.

If you have any questions concerning the installation or operation of this equipment, or if you need additional information, please call us at 770–844–4200.

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## **Insert Module into Base**

1. Pull base arm back to allow space for module to enter base

- 2. Align module slides with base track
- 3. Press module firmly into base



## Install Assembly on DIN Rail

4. Make sure the locking tab is in the latched position

5. Hook upper tab over upper flange of DIN rail

6. Tilt assembly toward DIN rail until module snaps securely to DIN rail



## Slide Assembly into Position on DIN Rail

7. Slide the module assembly on the DIN rail until the clip arm attaches securely to the adjacent module.

A.To remove the module from the base, lift the center of the base arm slightly outward and upward to release the module. Lifting the base arm further will eject the module. B.To remove the module assembly from the DIN rail, lift the clip arm up and slide the module assembly away from the adjacent module. Use a small screwdriver to pull the locking tab to the down position.

									00 (2 5)	
T1H– CTRIO Counte	er I/O Module	Note: Ap	nlv the labe	els that come	9.2 e with the I/O module	2 (0.36)		3.1 (0.32)	. 69 (3.5)	
Input Specifications:		to the I/O	base termi	nals to prop	erly identify the base	$\Gamma$				
Inputs	8 pts. ( 2 isolated channels / 4 pts. each channel), sink / source, 100K Hz max; See Input Resources Table for available Input Function options	terminal p	points.	Input / Out	put Channels					[2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14] [2:14
Minimum Pulse Width	5us	$\bigcirc$	000	0000	00000000	$\oslash$				54.3
Input Voltage Range	9–30VDC	1A 1	1B 1C 1D	Y0 Y0 Y1 Y	1 2A 2B 2C 2D Y2 Y2 Y3	Y3	2			
Maximum Voltage	30VDC						1A 1B	1C 1D Y0	Y1 2A 2B 2C 2D Y2 Y3	
Input Voltage Protection	Zener clamped at 33VDC							0 0 0 0		∄∦
Rated Input Current	8mA typical, 12mA maximum			Char	nnel Commons			$\otimes \otimes \otimes \otimes$	$\otimes \otimes $	<u>1</u>
Minimum ON Voltage	9.0VDC	$\oslash$	000	0000	00000000					.7(1.
Maximum OFF Voltage	2.0VDC	1M -	1M 1M 1M	C0 C0 C1 C	1 2M 2M 2M 2M C2 C2 C3	C3		$\circ$	$\otimes \otimes $	25.
Minimum ON Current	5.0mA @ 9.0VDC						.1 (0.32)			∃V
Maximum OFF Current	2.0mA		Lloor P	uo Torminolo	(no internal connection to CTPIO)		` <u>Ψ</u>			
OFF to ON Response < 3us									83.3 (3.28)	mm (in
On to OFF Response	< 3us			US 1						
Output Specifications:										
Outputs	4 pts., independently isolated, sink/source (FET Outputs); See Output Resources Table for available Output Function options							Ĩ		
Voltage Range	5–36VDC									
Maximum Voltage	36VDC								aida Viau	
Output Clamp Voltage	60VDC								Side view r	nm (in.)
Maximum Load Current	1.0A	LED Indicators						LED Desci	riptions	
Max. Leakage Current	100uA							ОК	Module OK	]
Inrush Current	5.0A for 20ms	LED Diagi	nostic Defin	itions				ER	User Program Error	1
OFF to ON Response	< 3us	ОК	ER	Descripti	on			CH1	Channel 1 Status	
ON to OFF Response	< 3us	ON	OFF	All is well –	- Run Mode			CHO	Channel 2 Status	1
ON State Voltage Drop	< 0.3V	ON	ON	Hardware I	Failure					-
External Power Supply	for loop power only, not required for internal module function	Blinking	Blinking	Boot Mode	- Use for Field OS Upgrades			1A–1D 2A–2D	Channel 1 A–D Status Channel 2 A–D Status	-
Overcurrent Protection	15A max	Blinking	OFF	Program M	lode			Y0 – Y3	Output Status	1
Thermal Shutdown	Tjunction = 150°C	OFF	Blinking	Module Se	lf –diagnostic Failure					1
	Tiunction = $130^{\circ}$ C	OFF	ON	Module Er	ror due to Watchdog Timeout	LED Diag	nostic D	efinitions		
Overtemperature Reset										-
Overtemperature Reset Duty Cycle Range	1% to 99% in 1% increments	OFF	OFF	No Power t	to Module	CH1	Blinks	when Chan	nel 1 Function 1 is counting	or timing
Overtemperature Reset Duty Cycle Range Operating Environment	1% to 99% in 1% increments           0°C-60°C, Humidity 5 to 95%	OFF	OFF	No Power t	to Module	CH1 CH2	Blinks Blinks	when Chan when Chan	nel 1 Function 1 is counting nel 2 Function 1 is counting	or timing



